



PATENT
Attorney Docket No.: SAM-0466
Customer No.: 29344

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jae-Hyung Lee
Serial No.: 10/645,018
Filing Date: August 21, 2003
Title: SEMICONDUCTOR MEMORY DEVICE HAVING PARTIALLY
CONTROLLED DELAY LOCKED LOOP

Examiner: Wells, Kenneth B.
Group Art Unit: 2816

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Post Office as First Class Mail on the date indicated below in an envelope addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

10-13-04
Date

Vanessa Marakas
Vanessa Marakas

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL LETTER

Sir:

Enclosed herewith for filing in the above-identified patent application please find the following listed items:

1. Amendment Transmittal;
2. Amendment A in response to Office Action mailed on July 26, 2004;
3. Check in the amount of \$230.00 to cover Additional Claims Fee; and
4. Return Postcard.

In connection with the foregoing matter, please charge any additional fees which may be due, or credit any overpayment, to Deposit Account Number 50-1798. A duplicate copy of this letter is provided for this purpose.

Respectfully submitted,

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Application Serial No.: 10/645,018
Reply to Office Action of: July 26, 2004

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Mail Stop Amendment
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AMENDMENT A

Sir:

The following is in response to the Office Action dated July 26, 2004, and is filed within the shortened statutory period of three months. Please amend the application as follows.

Amendments to the Specification begin on page 2 of this paper

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 10 of this paper.

10/18/2004 JADD01 00000037 10645018

01 FC:1202 54.00 OP
02 FC:1201 176.00 OP